

3/30/04

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	Not yet assigned 10/8 13,378
				Filing Date	Herewith 3/20/04
				First Named Inventor	VOGMAN
				Group Art Unit	Not yet assigned 2838
				Examiner Name	Not yet assigned Sterrett
Sheet	1	of	1	INTEL Docket Number	P18540

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JL	1	"TPS5210 Programmable Synchronous-Buck Regulator Controller", SLVS171A – September 1998, Revised May 1999, Copyright © 1999, Texas Instruments Incorporated, 29pgs.	
	2	Varga, Craig "Topology Tradeoffs and Layout Considerations for High Current Power Supplies, Ramblings of a Power Supply Designer", September 2003, National Semiconductor, The Sight & Sound of Information, 20pgs.	

Examiner Signature	Sterrett	Date Considered	3/2/06
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.